EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1070325	semiconductor same (fabrication or manufacturing or product or chip or ic or circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/11 12:20
L2	77	(preventS5 or defeat$4 or imped$6) near5 (reverse-engineer$4 or (reverse near3 engineer $4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/11 12:22
L3	22	L1 and L2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/11 12:22
L4	24675	L1 and ((secure-sensitive or security-sensitiv$3 or secur$4) with (chip or semiconductor))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/11 12:23
L5	3397	L1 and ((secure-sensitive or security-sensitiv$3 or secur$4) near3 (function$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/11 12:24
L6	4529	L1 and (security-sensitive or (secur$4 near5 protect$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/11 12:32
L7	228	L6 and dummy	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/11 12:32
L8	61	L7 and ((ic or circuit) near3 (function))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/11 12:32
L9	29	L8 and contacts	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/11 12:33

S1	252902	(smartcard or smart-card or (smart adj card)) or (smart$6 near3 (chip or circuit or ic or iC)) or (security-sensitive or (security near3 sensitiv$3)) or ((imped$5 or prevent $5) near3 (reverse-engineer$4 or (revers$3 near3 engineer$4))) or ((protect$4 or secur$6) near3 (ic or IC or circuit or chip))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/10 21:07
S2	854	S1 and ((dummy or non-function$4) near5 (fill$4 or component or material or structur$2)) or (dummy-fill)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/10 21:10
S3	828	S1 and (((dummy or non-function$4) near5 (fill$4 or component or material or structur$2)) or (dummy-fill))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/10 21:10
S4	51	S3 and ((adjacen$3 or along or (next adj to)) near3 (path or route))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/10 21:12
S5	722265	(activ$4 or powered) near3 (structure or device or component or cell)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/10 21:13
S6	18	S4 and S5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/10 21:13
S7	252902	(smartcard or smart-card or (smart adj card)) or (smart$6 near3 (chip or circuit or ic or iC)) or (security-sensitive or (security near3 sensitiv$3)) or ((imped$5 or prevent $5) near3 (reverse-engineer$4 or (revers$3 near3 engineer$4))) or ((protect$4 or secur$6) near3 (ic or IC or circuit or chip))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/10 21:24

S8	84655	S7 and (wafer or chip)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/10 21:24
S9	44511	S8 and semiconductor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/10 21:24
S10	2045	S9 and dummy	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/10 21:25
S11	49	S10 and (fill$4 near3 (structure))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/10 21:25
S12	524292	(secur$5 or security-sensitive) with (chip or substrate or device)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/10 21:30
S13	332947	(secur$5 or security-sensitive) near5 (chip or substrate or device)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/10 21:30
S14	336937	(secur$5 or security-sensitive) near5 (chip or substrate or device or ic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/10 21:30
S15	5640	reverse-engineer$4 or (reverse adj engineer $4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/10 21:31
S16	1695	S14 and S15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/10 21:31
S17	175	S16 and dummy	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/10 21:31

| S18 | 17 | S17 and ((fill$3 or dopant or implant$6) same structur$4) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/06/10 21:32 |

6/11/2009 12:37:50 PM
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